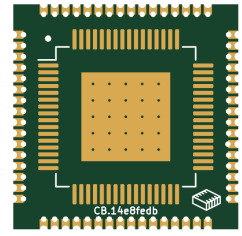


# Chip-Bridge Technologies

## CB LQFP-LQFP-64-A

Host: 64-LQFP 14 x 14mm — Guest: 64-LQFP 10 x 10mm



### Adapter Interfaces

Table 1: Adapter Parameters

Parameter	Host	Guest	Unit
Package	LQFP	LQFP	-
Pin Count	64	64	-
Package Dim.	14 x 14	10 x 10	mm
Pitch	0.8	0.5	mm

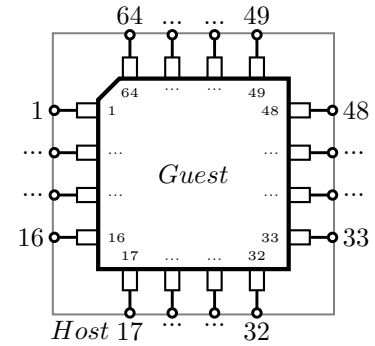


Figure 1: Adapter Pinout

### Features

- Drop-in adapter; Install 64-LQFP 10 x 10mm on a 64-LQFP 14 x 14mm footprint
- Low profile adapter, 0.8mm
- Supports common manufacturing methods
- 1:1 Pinout Configuration

### General Description

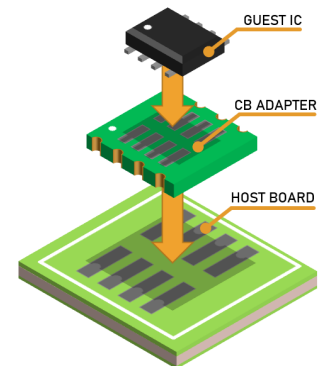
This device is a drop-in footprint to footprint adapter for your existing PBC design. Each Chip-Bridge Technologies adapter is designed to fit on the stated **Host Footprint**, and provide a **Guest Footprint** with electrical connections for your replacement IC.

Visit [chipbridgetech.com/products](https://chipbridgetech.com/products) to find our full product catalog. If you have questions or would like to request a design specific to your application, please contact our support team at [support@chipbridgetech.com](mailto:support@chipbridgetech.com).

**Chip-Bridge Technologies** Adapters are a patent pending design.

Host Pins	Guest Pins
1	1
2	2
3	3
4	4
...	...
61	61
62	62
63	63
64	64

Table 2: Pin Configuration



## Mechanical Specifications

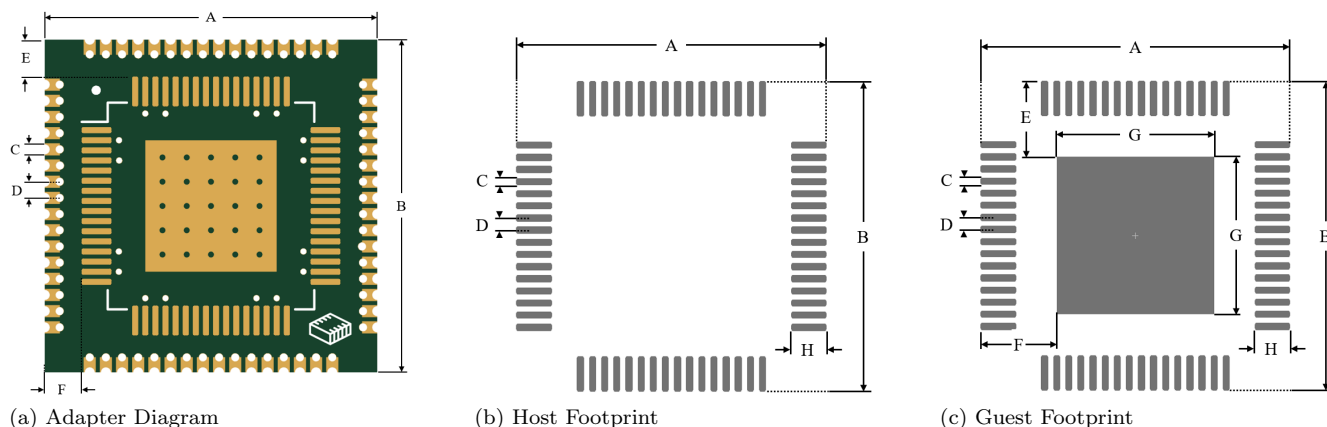


Figure 2: Mechanical Outline

Print version not to scale.

Table 3: Mechanical Specification

	Units	A	B	C	D	E	F	G	H	I
Adapter <sup>1</sup>	mm	16.45 ± 0.127	16.45 ± 0.127	0.55	0.8	1.825	1.825	-	-	-
Host Footprint <sup>1,2</sup>	mm	16.85	16.85	0.55	0.8	-	-	-	1.500	-
Guest Footprint <sup>1,3</sup>	mm	12.80	12.80	0.30	0.5	3.15	3.15	6.50	1.475	-

<sup>1</sup> Tolerances ±0.1mm unless otherwise stated.

<sup>2</sup> Host IC Ref. Drawing: <https://www.nxp.com/docs/en/package-information/SOT791-1.pdf>

<sup>3</sup> Guest IC Ref. Drawing: <https://www.nxp.com/docs/en/package-information/98ARH98426A.pdf>

## Trace Specifications

Table 4: Adapter Trace Specifications

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Trace Resistance	$R_{trace}$ <sup>4</sup>	0.1	2.7	15.0	mΩ	20°C
Trace to Trace Clearance	$d_{clearance}$		200 ± 13		μm	

<sup>4</sup> Calculated values.

## Part Identifier

Printed Identifier: 14e8fedb

## Datasheet Updates

You can find the latest datasheet at [chipbridgetech.com/products](https://chipbridgetech.com/products).

## Errata

1. v1.0: Initial Release
2. v1.1:
  - Add Errata section.
  - Update dead hyperlink to Guest IC Reference Drawing.