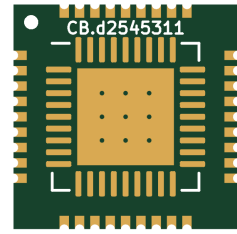# ⬚ Chip-Bridge Technologies

CB LQFP-QFN-36-A

Host: 36-LQFP 7 x 7mm — Guest: 36-QFN 6 x 6mm

Adapter Interfaces

Table 1: Adapter Parameters

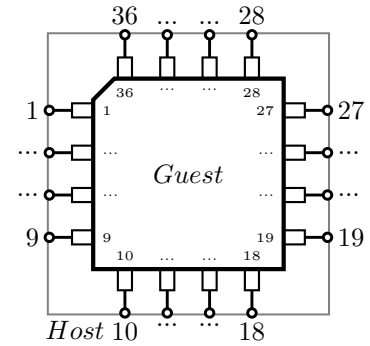| Parameter	Host	Guest	Unit
Package	LQFP	QFN	-
Pin Count	36	36	-
Package Dim.	7 x 7	6 x 6	mm
Pitch	0.65	0.5	mm

Figure 1: Adapter Pinout

Features

- Drop-in adapter; Install 36-QFN 6 x 6mm on a 36-LQFP 7 x 7mm footprint

- Low profile adapter, 0.8mm

- Supports common manufacturing methods

- 1:1 Pinout Configuration

General Description

This device is a drop-in footprint to footprint adapter for your existing PBC design. Each Chip-Bridge Technologies adapter is designed to fit on the stated **Host Footprint**, and provide a **Guest Footprint** with electrical connections for your replacement IC.

Visit chipbridgetech.com/products to find our full product catalog. If you have questions or would like to request a design specific to your application, please contact our support team at support@chipbridgetech.com.

Chip-Bridge Technologies Adapaters are a patent pending design.

Host Pins	Guest Pins
1	1
2	2
3	3
4	4
...	...
33	33
34	34
35	35
36	36

Table 2: Pin Configuration
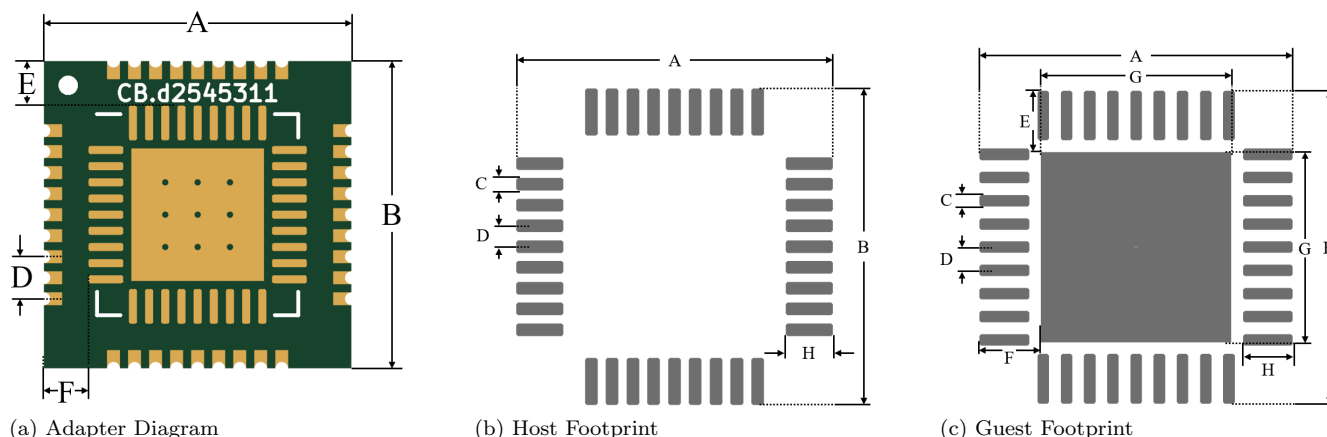
Mechanical Specifications

(a) Adapter Diagram
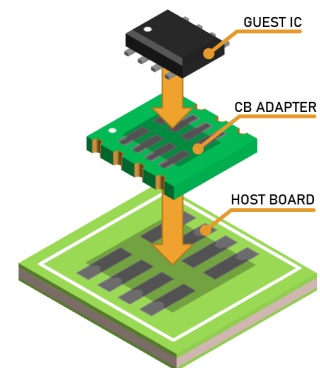
(b) Host Footprint

(c) Guest Footprint

Figure 2: Mechanical Outline

Print version not to scale.

Table 3: Mechanical Specification

	Units	A	B	C	D	E	F	G	H	I
Adapter[1]	mm	9.5 ± 0.127	9.5 ± 0.127	-	0.65	1.375	1.375	-	-	-
Host Footprint[1,2]	mm	9.9	9.9	0.4	0.65	-	-	-	1.475	-
Guest Footprint[1,3]	mm	6.75	6.75	0.25	0.5	1.325	1.325	4.1	1.075	-

[1] Tolerances ±0.1mm unless otherwise stated.
[2] Host IC Ref. Drawing: onsemi.com/pub/Collateral/561AV.PDF
[3] Guest IC Ref. Drawing: st.com/resource/en/datasheet/stm32f101t6.pdf#page=72

Trace Specifications

Table 4: Adapter Trace Specifications

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Trace Resistance	R_{trace}[4]	0.1	2.7	15.0	mΩ	20°C
Trace to Trace Clearance	$d_{clearance}$		250 ± 13		µm	

[4] Calculated values.

Part Identifier

Printed Identifer: d2545311

Datasheet Updates

You can find the latest datasheet at chipbridgetech.com/products.

Errata

1. v1.0: Initial Release

2. v1.1:
   - Add Errata section.
   - Update dead hyperlink to Guest IC Reference Drawing.