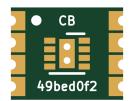


Chip-Bridge Technologies

CB SOIC-DFN-8-A

Host: 8-SOIC 3.9x4.9mm — Guest: 8-DFN 2x2mm



Adapter Interfaces

Table 1: Adapter Parameters

Parameter	Host	Guest	Unit
Package	SOIC	DFN	-
Pin Count	8	8	_
Package Dim.	3.9x4.9	2x2	mm
Pitch	1.27	0.5	mm



- Drop-in adapter; Install 8-DFN 2x2mm on a 8-SOIC 3.9x4.9mm footprint
- Low profile adapter, 0.8mm
- Supports common manufacturing methods
- 1:1 Pinout Configuration

General Description

This device is a drop-in footprint to footprint adapter for your existing PBC design. Each Chip-Bridge Technologies adapter is designed to fit on the stated Host Footprint, and provide a Guest Footprint with electrical connections for your replacement IC.

Visit chipbridgetech.com/products to find our full product catalog. If you have questions or would like to request a design specific to your application, please contact our support team at support@chipbridgetech.com.

Chip-Bridge Technologies Adapaters are a patent pending design.

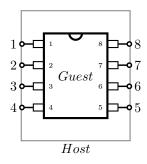
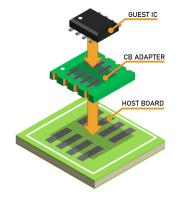


Figure 1: Adapter Pinout

Host Pins	Guest Pins
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8

Table 2: Pin Configuration



Mechanical Specifications

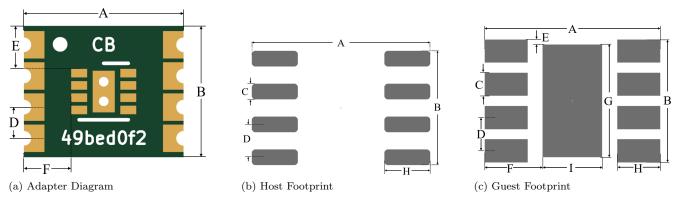


Figure 2: Mechanical Outline

Print version not to scale.

Table 3: Mechanical Specification

	Units	A	В	C	D	E	F	G	Н	I
$Adapter^1$	mm	6.50 ± 0.127	5.35 ± 0.127	-	1.27	1.0875	1.540	-	-	-
Host Footprint 1,2	mm	6.9	4.41	0.60	1.27	-	-	-	1.775	-
Guest Footprint 1,3	mm	2.650	1.850	0.35	0.5	-	-	1.7	0.65	0.9

 $^{^1}$ Tolerances $\pm 0.1 \mathrm{mm}$ unless otherwise stated.

Trace Specifications

Table 4: Adapter Trace Specifications

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Trace Resistance	R_{trace}^{4}	0.1	2.7	15.0	$\mathrm{m}\Omega$	20°C
Trace to Trace Clearance	$d_{clearance}$		100 ± 13		μm	

 $^{^4}$ Calculated values.

Part Identifier

Printed Identifer: 49bed0f2

² Host IC Ref. Drawing: www.ti.com/lit/ds/symlink/lm5017.pdf#page=35

³ Guest IC Ref. Drawing: www.st.com/resource/en/datasheet/lm2903.pdf#page=16

Datasheet Updates

You can find the latest datasheet at chipbridgetech.com/products.

Errata

- 1. v1.0: Initial Release
- 2. v1.1:
 - Add Errata section.
 - Update dead hyperlink to Host and Guest IC Reference Drawing.